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TC 2800 MAIL ROOM

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Brian Doyle

Title: QUANTUM WIRE GATE DEVICE AND METHOD OF MAKING SAME

Docket No.: 884.524US1

Serial No.: 09/516,653

Filed: March 1, 2000

Due Date: February 7, 2002

Examiner: Neal Berezny

Group Art Unit: 2823

Commissioner for Patents
Washington, D.C. 20231

We are transmitting herewith the following attached items (as indicated with an "X"):

- A return postcard.
- An Amendment and Response Under 37 CFR 1.111 (19 Pages).
- Clean Version of Pending Claims (10 pgs.).
- Clean Version of Amended Specification Paragraphs (7 pgs.).
- Red-line figures (8 pgs.).

Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on this 7 day of February, 2002.

Jane E. Brockschink

Name

Jane E. Brockschink
Signature

Customer Number 21186

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

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(GENERAL)



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PRELIMINARY REMARKS

Applicant expresses his appreciation to the Examiner for his careful review of the specification and claims in the Office Action mailed on November 7, 2001. The Examiner's review has alerted the Applicant to typographical errors that are rectified by presentation of this Amendment. The Applicant has reviewed the Office Action mailed on November 7, 2001. Please amend the above-identified patent application as follows.

IN THE SPECIFICATION

At page 2, line 1, please replace the heading as follows.

[BACKGROUND OF THE INVENTION] BACKGROUND INFORMATION

However, please do not provide an underline for the amended heading.

At page 2, line 6, please amend the heading as follows.

[Description of Related Art] TECHNICAL FIELD

However, please do not provide an underline for the amended heading.

At page 2, beginning at line 16, please replace the paragraph as follows:

A field effect transistor (FET) is a fundamental building block of integrated circuits.